FIG. 1 (PRIOR ART)

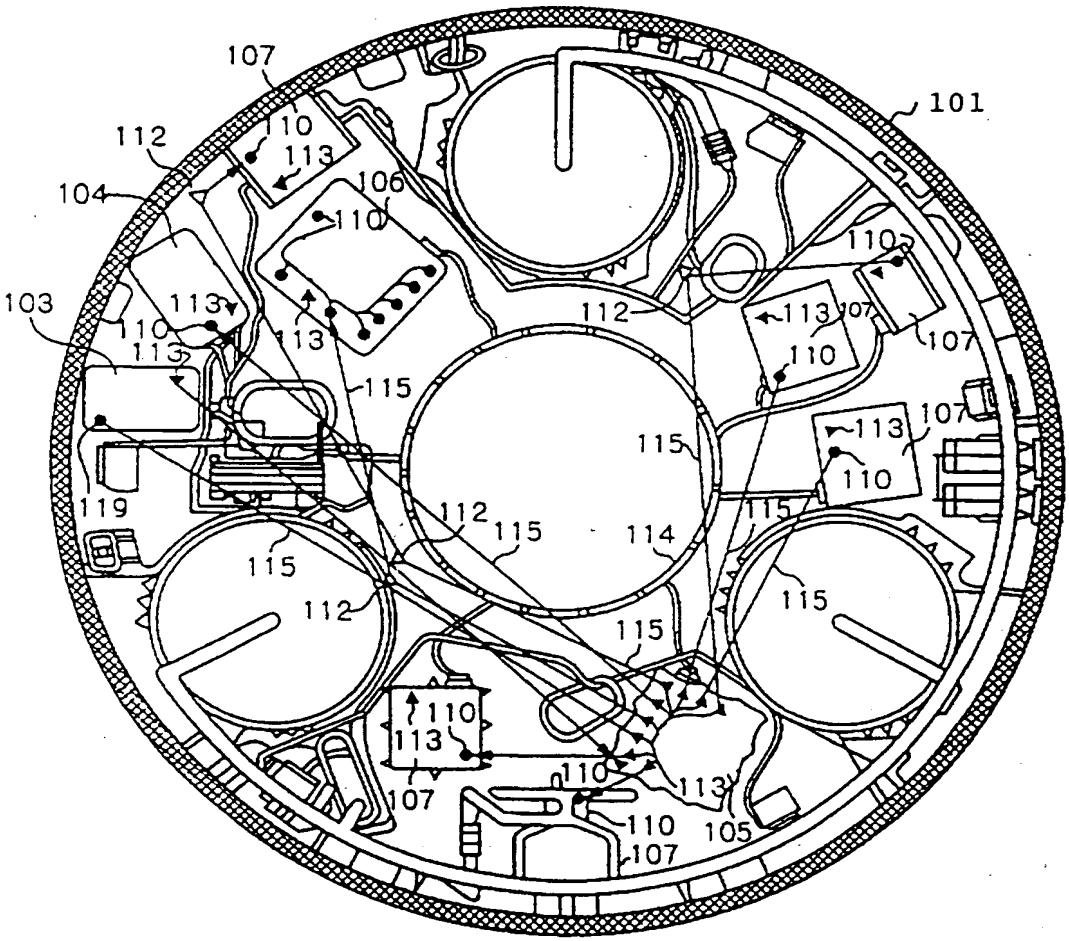
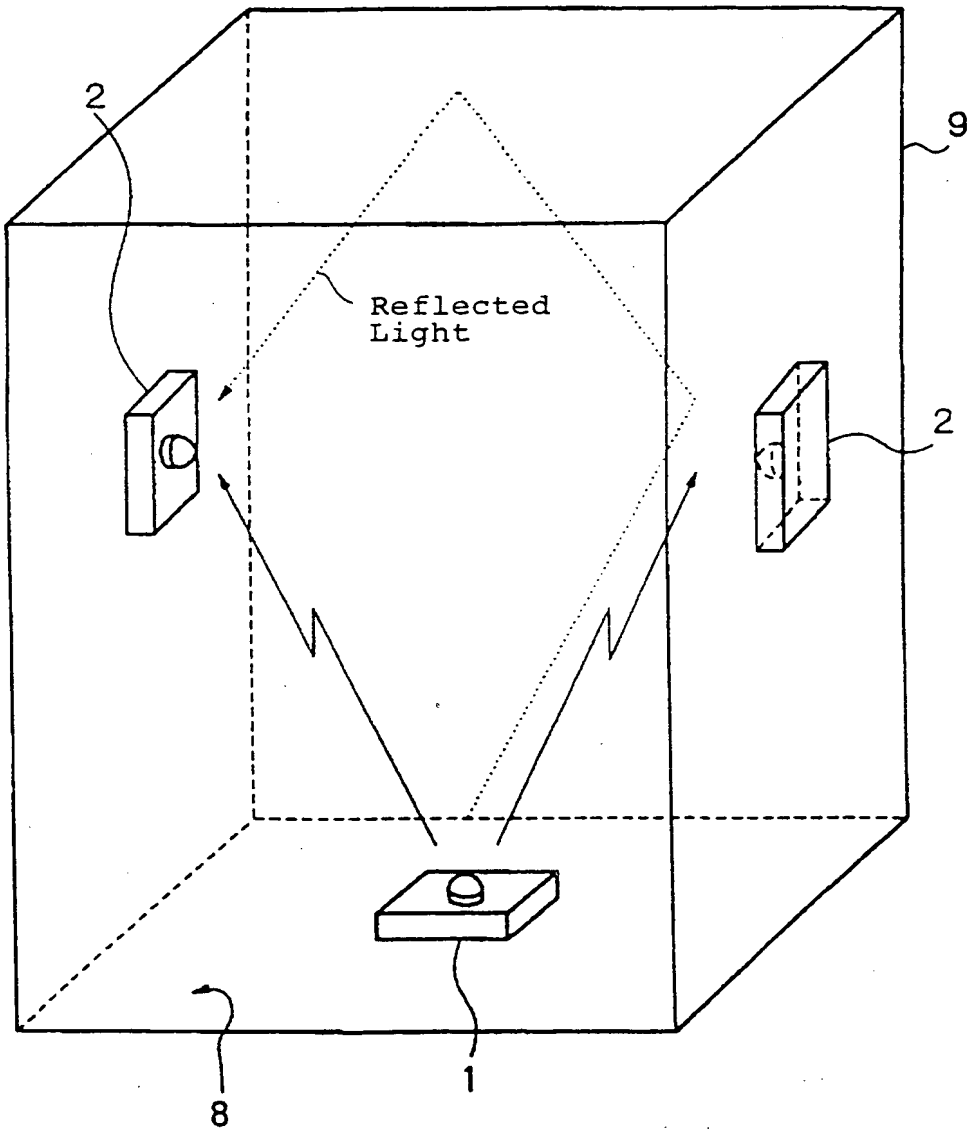
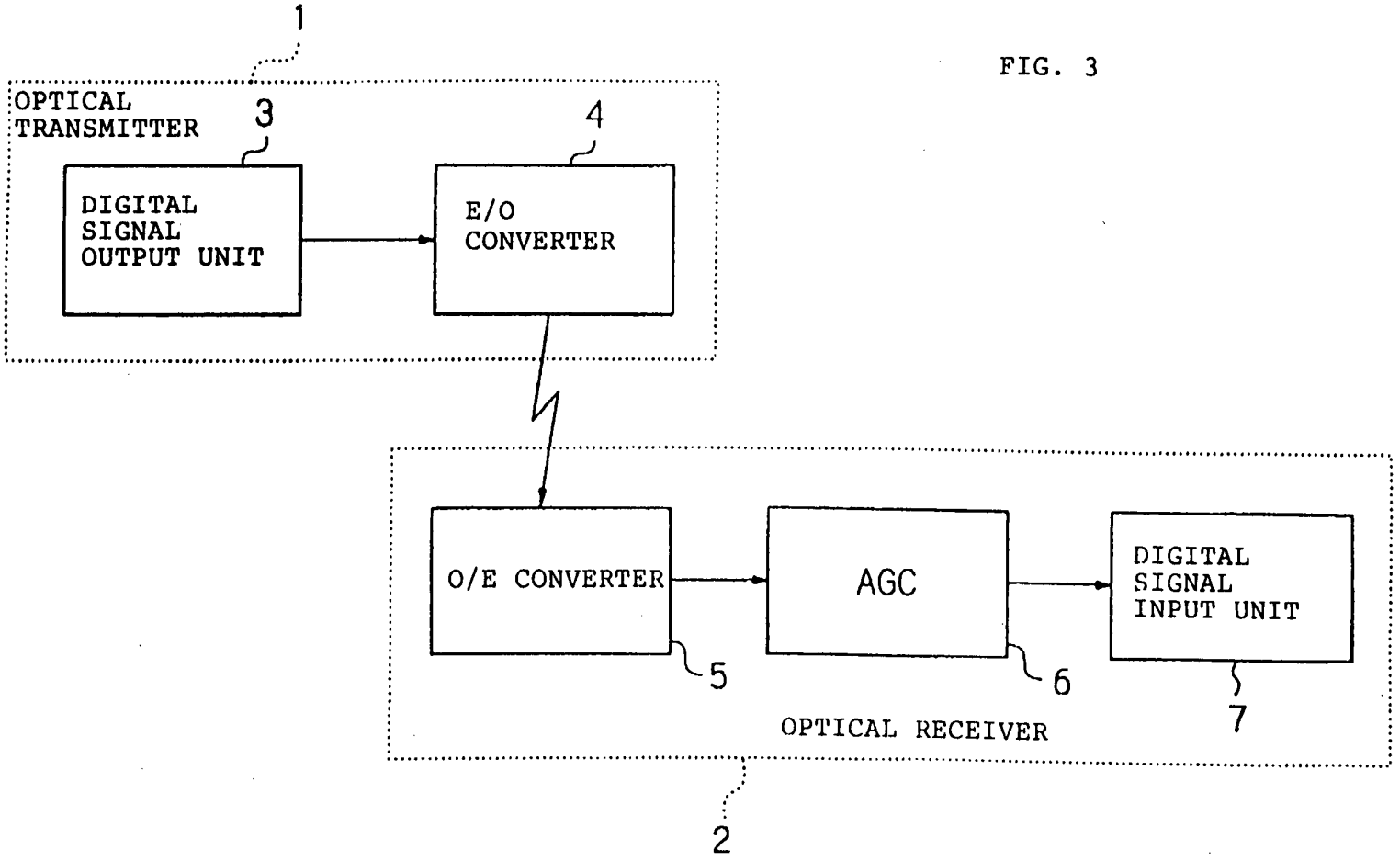


FIG. 2



TOKYO 6572660

FIG. 3



JP2007-267360

FIG. 4

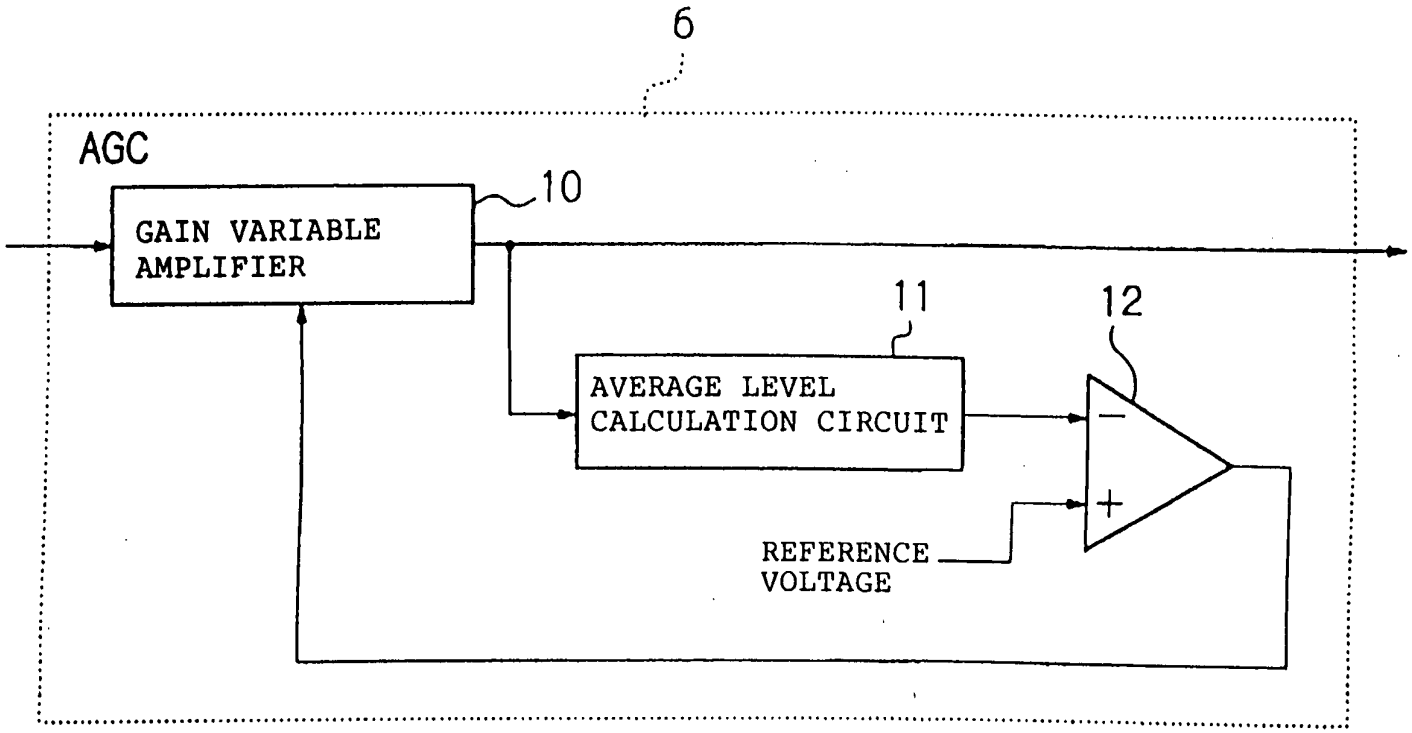


FIG. 5a

INPUT OF AGC6

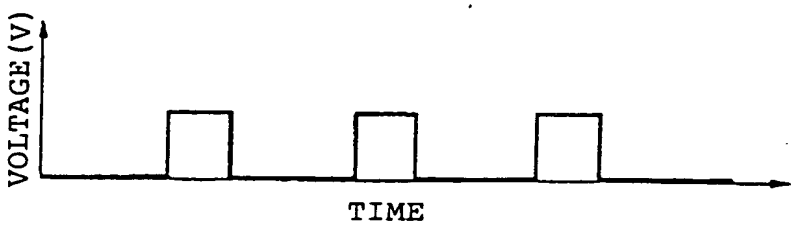


FIG. 5b

OUTPUT OF AVERAGE LEVEL CALCULATION CIRCUIT 11

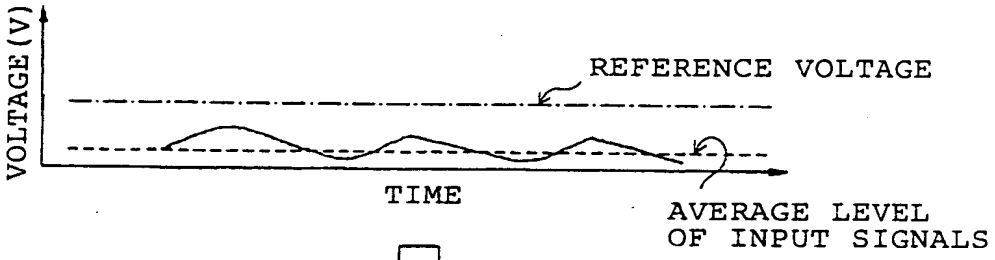


FIG. 5c

OUTPUT OF AGC6

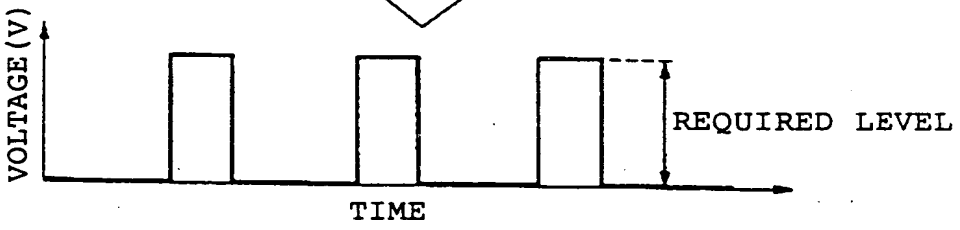


FIG. 6

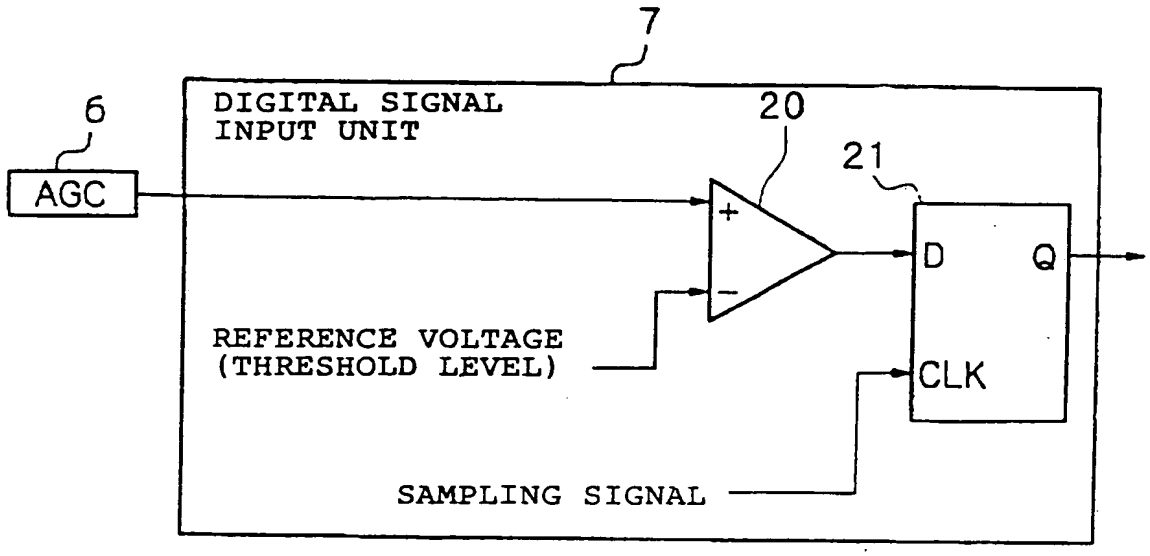
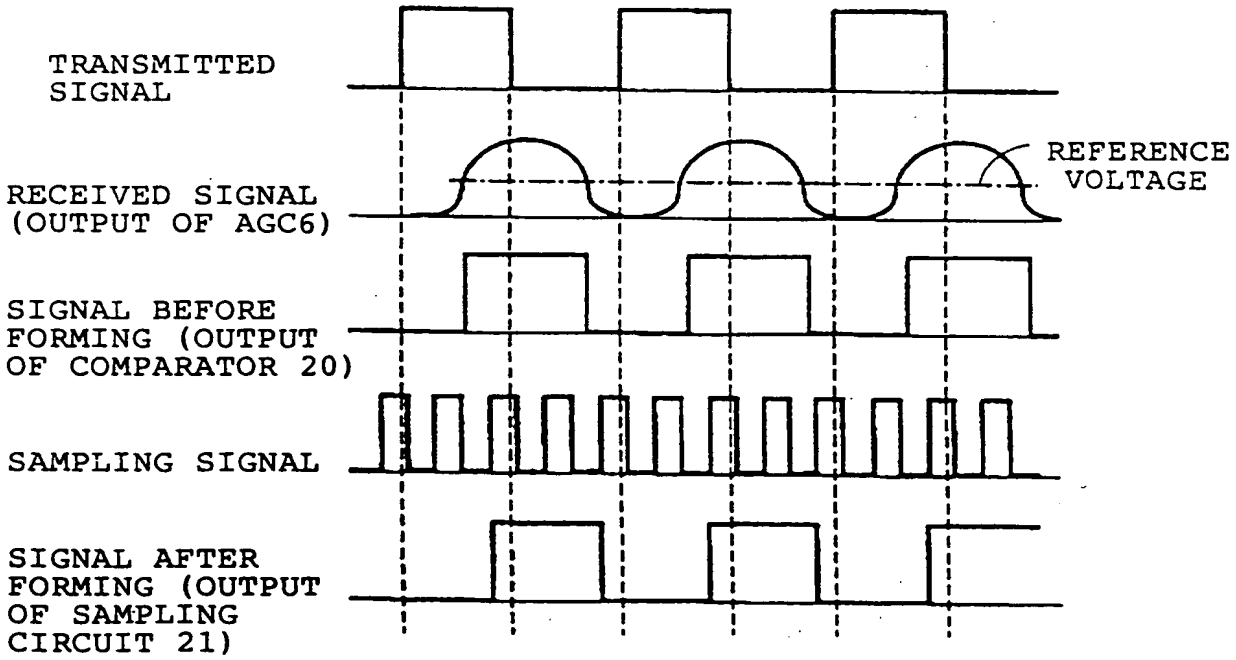

7
6
DIGITAL SIGNAL INPUT UNIT
20
21
AGC
+
−
D
Q
REFERENCE VOLTAGE (THRESHOLD LEVEL)
CLK
SAMPLING SIGNAL

FIG. 7


TRANSMITTED SIGNAL
REFERENCE VOLTAGE
RECEIVED SIGNAL (OUTPUT OF AGC6)
SIGNAL BEFORE FORMING (OUTPUT OF COMPARATOR 20)
SAMPLING SIGNAL
SIGNAL AFTER FORMING (OUTPUT OF SAMPLING CIRCUIT 21)